

## ABSTRACT OF THE DISCLOSURE

5       An apparatus and method for making a nanopore chip exhibiting one of low  
      photosensitivity, low electrical noise, and low electrical drift. The apparatus provides a  
      thin insulating diaphragm containing a nanopore, the diaphragm being supported on a  
      rigid semiconductor frame, the semiconductor frame having N-type doping in those  
      regions which are to be capacitively coupled to an ionic solution. Also disclosed is a  
      method of making the apparatus.

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